

DO NOT ENTER 11/21/05 TM

IN THE CLAIMS

Please amend the claims as follows.

1. (Previously Presented) A data processor having a clustered architecture comprising:

a branching cluster and a non-branching cluster, each capable of executing instructions and computing branch conditions, said branching cluster operable to perform branch address computations for said branching cluster and said non-branching cluster, the non-branching cluster incapable of performing branch address computations; and

remote conditional branching control circuitry that causes said branching cluster to perform a branch address computation in response to sensing a conditional branch instruction in said non-branching cluster, and that communicates a computed branch condition from said non-branching cluster to said branching cluster.

2. (Original) The data processor as set forth in Claim 1 wherein each of said branching cluster and said non-branching cluster comprises at least one register file.